

# Are we done with Ray Tracing?

Alexander Keller

## Schedule

Course web page at https://sites.google.com/view/arewedonewithraytracing

- 9:00 Are we done with Ray Tracing?
  - Alexander Keller, NVIDIA
- 9:40 Acceleration Data Structure Hardware (and Software)
  - Timo Viitanen, NVIDIA
- 10:15 State-of-the-Art and Challenges in Game Ray Tracing
  - Colin Barré-Brisebois, SEED Electronic Arts
- break
- 11:05 Reconstruction for Real-Time Path Tracing
  - Christoph Schied, Facebook Reality Labs
- 11:40 From Raster to Rays in Games
  - Morgan McGuire, NVIDIA



#### **Principles of Image Synthesis**

Rasterization

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#### **Principles of Image Synthesis**

Rasterization





#### **Principles of Image Synthesis**

Rasterization . . . . ٠ . . . . . . . . . . ٠ . . . . . . . . . . ٠ . . ۰ ٠ . . . . . 11 clipping



#### **Principles of Image Synthesis**

Rasterization . . . . . . ٠ . . . . . . . . . . . . . . . . ٠ ٠ . . . . . . . . ٠, clipping Z-buffer



#### **Principles of Image Synthesis**

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#### **Principles of Image Synthesis**





#### **Principles of Image Synthesis**



#### **Principles of Image Synthesis**





# **Ray Tracing**

How it started

- 1974: "brute-force approach" of rasterization "ridiculously expensive"
  - A characterization of ten hidden-surface algorithms

"Ray tracing is the future and ever will be"

SIGGRAPH 2013 Course



# Ray Tracing

How it started

1974: "brute-force approach" of rasterization "ridiculously expensive"

"Ray tracing is the future and ever will be"

The Path Tracing Revolution in Movie Industry

SIGGRAPH 2015 Course
ACM Transactions on Graphics, Volume 37 Issue 3, August 2018
Vectorized Production Path Tracing
The Iray Light Transport Simulation and Rendering System
SIGGRAPH 2018 Course

SIGGRAPH 2013 Course

A characterization of ten hidden-surface algorithms

#### 1995: ART's RenderDrive: Ray Tracing Hardware from before the GPU

- top of bounding volume hierarchy (BVH) cached in each processor
  - geometry streamed against ray buffers
  - processors flag bounding volume intersection to demand broadcast of children
    - · processors running idle when rays diverge
    - new rays can be started while tracing old ones





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    - · processors running idle when rays diverge
    - $\cdot \,$  new rays can be started while tracing old ones
- shading processor
- rays sorted by shader in order to increase coherency
- fire-and-forget ray tracing: Results added to pixels



Cold Chips: ART's RenderDrive



#### 2005: Ray Processing Unit

architecture



▶ RPU: A Programmable Ray Processing Unit for Realtime Ray Tracing



#### 2005: Ray Processing Unit

#### architecture



▶ RPU: A Programmable Ray Processing Unit for Realtime Ray Tracing



#### 2014: Imagination Technologies

#### architecture



Introduction to PowerVR Ray Tracing



#### 2014: Imagination Technologies

queuing rays to nodes





#### 2014: Imagination Technologies

- For each VBExtents triangle Foreach Select LOD VBNode in LOD Compute Parent Voxel BerLOD VBNode VBNode Pool On VoxelCache linked list HIT Pool head LT pointers Add VBNode Output Scene Acceleration Leaf Tree Structure VoxelCache VoxelCache GenerateVBNode() AssembleParents() ProcessTriangle() ıdc14
- streaming bounding volume hierarchy construction

Introduction to PowerVR Ray Tracing



#### 2014: Imagination Technologies

streaming bounding volume hierarchy construction



Introduction to PowerVR Ray Tracing



#### **Efficient culling**

partitioning space: *k*d-tree or (nested) uniform grids





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partitioning object lists: convex bounding volumes, e.g. axis aligned boxes, spheres, planes, ...





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Parallel bounding volume hierarchy (BVH) construction

linear bounding volume hierarchy (LBVH)



#### Parallel bounding volume hierarchy (BVH) construction

- linear bounding volume hierarchy (LBVH)
  - map object centroids  $(p_x, p_y, p_z)$  onto the unit cube
  - interleaving the bits of the quantized coordinates yields the Morton code of  $(p_x, p_y, p_z)$
  - enumerating the sorted Morton code keys corresponds to a traversal along the Z-curve



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Thinking Parallel, Part III: Tree Construction on the GPU



Parallel bounding volume hierarchy (BVH) construction

n sorted Morton code keys





Parallel bounding volume hierarchy (BVH) construction

n sorted Morton code keys stored in an array of leaves





#### Parallel bounding volume hierarchy (BVH) construction

• xor-ing neighboring Morton codes yields a metric of how close they are along the Z-curve





#### Parallel bounding volume hierarchy (BVH) construction

• the height of the n-1 inner nodes represents this metric, which can be computed on-the-fly





#### Parallel bounding volume hierarchy (BVH) construction

• each leaf is assigned an interval, whose boundaries reference its neighboring inner nodes





#### Parallel bounding volume hierarchy (BVH) construction

parent of a node is the most similar one of the two inner nodes indexed by interval boundaries





#### Parallel bounding volume hierarchy (BVH) construction

• whenever both children of an inner node are set, its parent can be determined the same way





#### Parallel bounding volume hierarchy (BVH) construction

Ieft children propagate up their left interval boundary (right children in analogy)





#### Parallel bounding volume hierarchy (BVH) construction

• the root node interval spans the range of leaves and is copied to the inner node with index 0




#### **Ray Tracing**

Frameworks and application programming interfaces (API)

Ray Tracing Engine

High performance ray tracing kernels

- High-efficiency, high performance heterogeneous Ray Tracing Intersection Library

AMD FireRays SDK

NVIDIA OptiX

Intel Embree

#### **Ray Tracing**

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The Quest for the Ray Tracing API

- High-efficiency, high performance heterogeneous Ray Tracing Intersection Library
- AMD FireBays SDK

SIGGBAPH 2016 Course Introduction to NVIDIA RTX and DirectX Ray Tracing Introduction to Real-Time Ray Tracing with Vulkan Metal for Ray Tracing Acceleration

NVIDIA OptiX

Intel Embree

# Surface Representation Meshes

- patchwork geometry
  - irregular topology at top level
  - regular topology at bottom level
- watertight along shared edges
  - numerical issues
  - compression by quantization





# Surface Representation Meshes

- patchwork geometry
  - irregular topology at top level
  - regular topology at bottom level
- watertight along shared edges
  - numerical issues
  - compression by quantization
- subdivision surfaces
  - adaptive subdivision
  - mathematically meaningful





Massively Parallel Stackless Ray Tracing of Catmull-Clark Subdivision Surfaces



**Displacement or no displacement** 

diffuse texture





**Displacement or no displacement** 

normal mapping





**Displacement or no displacement** 

parallax occlusion mapping





**Displacement or no displacement** 

displacement mapping





#### **Displacement or no displacement**

base mesh: 8,750 vertices and 16,636 vertex indices



Geometry courtesy Henning Sanden



#### **Displacement or no displacement**

displaced mesh: 6,327,463 vertices and 12,629,248 vertex indices



Geometry courtesy Henning Sanden



**Displacement or no displacement** 





**Displacement or no displacement** 





**Displacement or no displacement** 





**Displacement or no displacement** 





**Displacement or no displacement** 





**Displacement or no displacement** 

level of detail may affect visibility



knowing the level of detail





**Floating point arithmetic** 

- define 0/0, avoid exceptions, and use consistent numeric formulations
- logarithmic spacing of floating point numbers





#### Floating point arithmetic

- define 0/0, avoid exceptions, and use consistent numeric formulations
- logarithmic spacing of floating point numbers
  - ray direction and scale matter







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#### Dealing with the ill-posed self intersection problem

- post iteration to refine hitpoint and guarantee side of plane
- robust epsilon for offsetting



It's really not a rendering bug, you see...



#### Amortizing and balancing the work of a frame

- preprocessing
  - bounding volume hierarchy construction and update on demand

ray tracing and shading

- postprocessing
  - filtering
  - upscaling



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#### Amortizing and balancing the work of a frame

- preprocessing
  - bounding volume hierarchy construction and update on demand
  - parameter baking on demand
- ray tracing

- shading
- postprocessing
  - filtering
  - upscaling



#### Amortizing and balancing the work of a frame

- preprocessing
  - bounding volume hierarchy construction and update on demand
  - parameter baking on demand
- ray tracing
- shadow rays
- shading
- postprocessing
  - filtering
  - upscaling



#### Geometry

- overlapping bounding volumes
  - partitioning vs. memory footprint
  - e.g. foliage, fur, hair





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- high valence vertices, e.g. triangle fans
- silhouettes, i.e. the cost of missing geometry
  - similar for ray tracing distance fields





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- parametric, procedural, and on-demand geometry
  - instances
  - skinning





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#### **Geometry and Shading**

• geometry vs.  $\alpha$ -maps or even procedural trimming and shading



```
float = = atan(q, x, q, y);
float = = longth(q)
float = = longth(q)
float = songth(q)
float q = sin(1.574.3.0*a+iGlobalTime);
float d = 0.15 + 0.1*aqrt(s) + 0.15*g*g;
float h = r(1.0-smoothstep(0.95, 1.0, h))
h = 1.0-0.5*(1.0-h)*amoothstep(0.95+0.05*h, 1.0, sin(3.0*a+iGlobalTime));
vecl hcol = vecl(0.5+0.1*q, y(1.0, 0.5+0.1*q, y);
hcol*= 1.0 - 0.5*r;
h = 0.1 + h)
vecl o = 1. + h;
```

Inigo Quilez



#### Ray Tracing Performance Shading

- incoherent texture access
  - stochastic interpolation of bilinear texture interpolation

Vectorized Production Path Tracing



# Ray Tracing Performance Shading

- incoherent texture access
  - stochastic interpolation of bilinear texture interpolation

- strict separation of material definition and rendering algorithms
  - generic BSDF models
  - baking BSDF parameters



The Material Definition Language
 GI Next: Global Illumination for Production Rendering on GPUs
 Manuka: A Batch-Shading Architecture for Spectral Path Tracing in Movie Production





#### What happened since Breakpoint 2005?

• 512 x 384 pixels at 5-10 fps on a Pentium 4M, everything computed live from scratch



> To trace or not to trace - that is the question. State of the art in fast ray tracing (FaRT).



#### Load balancing





#### Load balancing





#### Load balancing





#### Load balancing





#### Load balancing





#### Load balancing

scanline-based workload distribution





#### Load balancing

- non-uniform cost of pixels/samples
  - not known a priori and thus cannot be taken into account to compute the optimal distribution of work

- non-uniform performance of processing units
  - common in systems that combine GPUs from different generations with a CPU and in network clusters



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#### Load balancing

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  - $\Rightarrow$  assign a fixed subset of the workload to each processor per frame
- non-uniform performance of processing units
  - common in systems that combine GPUs from different generations with a CPU and in network clusters
- $\Rightarrow$  based on performance measurements, adapt size of the subset between frames



#### Ray Tracing Performance Load balancing

• adaptive striping with relative performance weights  $w_k$  of 10%, 50%, 25%, and 15%





#### Ray Tracing Performance Load balancing

• adaptive striping with relative performance weights  $w_k$  of 10%, 50%, 25%, and 15%



- note how the headlight pixels are distributed among processing units

Ray Tracing Gems, Ch. 10





#### The headlight challenge: Sequential singular chains

#### Are we done with Ray Tracing?

#### Not quite yet...

- mathematically meaningful level of detail
- separation of shading and geometry
- scalable parallel algorithms for singular chains



#### Are we done with Ray Tracing?

#### Not quite yet...

- mathematically meaningful level of detail
- separation of shading and geometry
- scalable parallel algorithms for singular chains

and of course, ray tracing means sampling



#### Are we done with Ray Tracing?

#### Ray tracing is here to stay

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